

**ABSTRACT**

A liquid crystal display panel capable of improving a picture quality and reducing power dissipation includes a plurality of liquid crystal cells each having a thin film transistor formed at a crossing of gate lines and data lines, and a pixel electrode connected to the thin film transistor; a first parasitic capacitor formed between a data line and an adjacent preceding or succeeding pixel first electrode; a second parasitic capacitor formed between the data line and an preceding or succeeding pixel second electrode; and a groove formed at a portion of the pixel electrode adjacent the data line and opposing a portion coupled to the thin film transistor.